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002/009

Attorney's Docket No.: Intel 10559-881001/P17483

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yan Borodovsky Art Unit: 1756
Serial No.: 10/681,031 Examiner: unknown
Filed : October 7, 2003 Assignee: Intel Corporation

Title : COMPOSITE OPTICAL LITHOGRAPHY METHOD FOR PATTERNING
 LINES OF SUBSTANTIALLY EQUAL WIDTH

VIA FACSIMILE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information
Disclosure Statement and document listed on form PTO-1449.

This filing is being made before the receipt of a first
Office action on the merits. No fee is required.

The documents are in the English language; hence no concise
explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the
return of a copy of the enclosed form PTO-1449 with the

CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by
facsimile to the Patent and Trademark Office on the date indicated
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Date of Transmission January 21, 2005

Signature 

Carroll Altman
Typed or Printed Name of Person Signing Certificate

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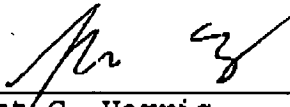
Attorney's Docket No.: Intel 10559-811001
/P17482

Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit
Account No. 06-1050.

Respectfully submitted,

Date: January 21, 2005



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Sheet 1 of 1

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| Substitute Form PTO-1449 (Modified) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. 10559-881001 | Application No. 10/681,031 |
| Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | | Applicant Yan Borodovsky | |
| | | Filing Date October 7, 2003 | Group Art Unit 1756 |

| U.S. Patent Documents | | | | | | | |
|-----------------------|-----------|-----------------|------------------|----------|-------|----------|----------------------------|
| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee | Class | Subclass | Filing Date If Appropriate |
| | AA | | | | | | |
| | AB | | | | | | |
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| Foreign Patent Documents or Published Foreign Patent Applications | | | | | | | | |
|---|-----------|-----------------|------------------|--------------------------|-------|----------|-------------|----|
| Examiner Initial | Desig. ID | Document Number | Publication Date | Country or Patent Office | Class | Subclass | Translation | |
| | | | | | | | Yes | No |
| | AL | | | | | | | |
| | AM | | | | | | | |
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| | AP | | | | | | | |

| Other Documents (Include Author, Title, Date, and Place of Publication) | | |
|---|-----------|---|
| Examiner Initial | Desig. ID | Document |
| | AQ | "Gratings of regular arrays and trim exposures for ultralarge scale integrated circuit phase shift lithography"; Fritze et al.; Journal Vacuum. Sci Technology B; Vol. 19, No. 6, pp. 1366-1370; Nov/Dec 2001 |
| | AR | |
| | AS | |
| | AT | |

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| Examiner Signature | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |

Substitute Disclosure Form (PTO-1449)